

**REMARKS/ARGUMENTS**

Reconsideration of the present application, as amended, is respectfully requested.

The Abstract had been amended as requested by the Examiner. After reviewing the Abstract, the Applicants have amended it again for a more precise statement of the described CMOS integrated circuit.

With respect to pending claims 12-44 and 46-62, claims 15-44 and 46-62 were allowed. Claims 12-14 were rejected under the judicially created doctrine of obviousness-type double patenting over claims 18, 21 and 24 of U.S. Patent No. 6,041,010. The Applicants herewith files a Terminal Disclaimer under 37 C.F.R. §1.321 for the allowance of claims 12-14.

Finally, the Applicants apologetically submit a supplemental Information Disclosure Statement. Most of the references had been previously cited in the current application's ancestral and related applications and resulting patents, i.e., U.S. Patent Nos. 5,650,955; 5,703,806; 6,041,010; 6,356,497; and 6,771,532. The references had been inadvertently omitted from the Information Disclosure Statement submitted on March 18, 2004. An additional eight references in the supplemental Information Disclosure Statement (Citation Nos. CB, CD, CF, CI, DD, DE, DF, and DG) were recently brought to the Applicants' attention; copies of these references or English translations of which (DE and DF) are included herewith.

Therefore, in view of the amendments above and the remarks directed thereto, the Applicants respectfully request that the rejections be removed, that claims 12-44 and 46-62 be allowed, and the case be passed to issue. If a telephone conference would in any way expedite

Appl. No. 10/803,783  
Amendment Dated January 21, 2005  
Reply to Office Action of October 21, 2004

the prosecution of the application, the Examiner is asked to call the undersigned at (408) 446-7687 to discuss this matter.

Respectfully submitted,



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